|  |  |  |  |  |  |  |  |  |
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| 7 PV DE TEST HUBVIEW-CPU-ESCLAVE-MODULE MODULE CPU-ESCLAVE   |  |  |  |  |  | | --- | --- | --- | --- | --- | |  | Ce document est la propriété d'ACTIA Telecom et ne peut être reproduit ou communiqué sans autorisation écrite. | REFERENCE : **X301397** | INDICE : **4** | PAGE  **17** **/** **19** |   Date : [date]  Opérateur : [operateur]  S/N Module (X130360340) : [numero] | | | |
| **PV** **de** **TEST** **(page** **1/3)** | | |  |
|  | | |
| **Paramètres** | **Sanctions** | **Mesures** |
|  | | |
| **Version** **du** **BIOS** **de** **la** **carte** **ComExpress** | | |
| BIOS Version | NA | [bios\_version] |
| Configuration de la variable « User I2C Support » | Variable = Controller-based PCI mode | OK |
| Config. variable « SD-Card / GPIO selection » | Variable = SD-Card | OK |
| Configuration de la variable « HD-Audio Support » | Variable = Disabled | OK |
| Configuration de la variable « QuietBoot » | Variable= Enabled | OK |
|  | | |
| **test de bon fonctionnement** | | |
|  | | |
| **Test des LEDS :** | | |
| Choix 3: Leds test : ON GREEN | LED Signe de vie: Allumée Verte | [leds\_vertes] |
| LED Alarme: Allumée Verte | [leds\_vertes] |
| LED Caméra: Allumée Verte | [leds\_vertes] |
| LED MemView: Allumée Verte ou éteinte | [leds\_vertes] |
| Choix 4: Leds test : ON RED | LED Signe de vie : Allumée Verte | [leds\_rouges] |
| LED Alarme: Allumée Rouge | [leds\_rouges] |
| LED Caméra: Allumée Rouge | [leds\_rouges] |
| LED MemView: Allumée rouge ou orange | [leds\_rouges] |
| Choix 5: Leds test : OFF | LED Signe de vie : Eteinte | [leds\_off] |
| LED Alarme : Eteinte | [leds\_off] |
| LED Caméra : Eteinte | [leds\_off] |
| LED MemView: Allumée Verte ou éteinte | [leds\_off] |
| **Tests des commandes CMD\_PWR\_RECORDER et CMD\_LED\_POWER\_OUT :** | | |
| Choix u: On MEMView power | LED D1 sur carte outillage FDP allumée | [led\_D1] |
| LED D4 sur carte outillage FDP allumée | [led\_D4] |
| Choix m: Off MEMView power | LED D1 sur carte outillage FDP éteinte | [led\_D1] |
| LED D4 sur carte outillage FDP éteinte | [led\_D4] |
| **Tests des GPIO** | | |
| S6.2 = OFF | PRESENCE\_V\_BKP : 0 (rd ok) | [switch1] |
| S6.2 = ON | PRESENCE\_V\_BKP : 1 (rd ok) | [switch1] |
| S5.1 = OFF | PRESENCE\_12V\_MSA : 0 (rd ok) | [switch2] |
| S5.1 = ON | PRESENCE\_12V\_MSA : 1 (rd ok) | [switch2] |
| S5.2 = OFF | PRESENCE\_12V : 0 (rd ok) | [switch3] |
| S5.2 = ON | PRESENCE\_12V : 1 (rd ok) | [switch3] |
| Interrupteur en face avant position gauche | COM\_ROT1\_P1 : 0 (rd ok) | [switch4] |
| COM\_ROT1\_P2 : 1 (rd ok) | [switch4] |
| COM\_ROT2\_P1 : 0 (rd ok) | [switch5] |
| COM\_ROT2\_P2 : 1 (rd ok) | [switch5] |
| Interrupteur en face avant position droite | COM\_ROT1\_P1 : 1 (rd ok) | [switch6] |
| COM\_ROT1\_P2 : 0 (rd ok) | [switch6] |
| COM\_ROT2\_P1 : 1 (rd ok) | [switch7] |
| COM\_ROT2\_P2 : 0 (rd ok) | [switch7] |
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| **PV** **de** **TEST** **(page** **2/3)** | | | | | |  |
|  | | | | | |
| **Paramètres** | | | | **Sanctions** | **Mesures** |
|  | | | | | |
| **Tests** **des** **GPIO** **(suite)** | | | | | |
| S1.2 = OFF | | | | CFG\_MSA11\_0 : 1 (rd ok) | [switch8] |
| S1.2 = ON | | | | CFG\_MSA11\_0 : 0 (rd ok) | [switch8] |
| S1.1 = OFF | | | | CFG\_MSA11\_1 : 1 (rd ok) | [switch9] |
| S1.1 = ON | | | | CFG\_MSA11\_1 : 0 (rd ok) | [switch9] |
| S7.2 = OFF | | | | CFG\_MSA12\_0 : 1 (rd ok) | [switch10] |
| S7.2 = ON | | | | CFG\_MSA12\_0 : 0 (rd ok) | [switch10] |
| S7.1 = OFF | | | | CFG\_MSA12\_1 : 1 (rd ok) | [switch11] |
| S7.1 = ON | | | | CFG\_MSA12\_1 : 0 (rd ok) | [switch11] |
| S3.2 = OFF | | | | CFG\_MSA21\_0 : 1 (rd ok) | [switch12] |
| S3.2 = ON | | | | CFG\_MSA21\_0 : 0 (rd ok) | [switch12] |
| S3.1 = OFF | | | | CFG\_MSA21\_1 : 1 (rd ok) | [switch13] |
| S3.1 = ON | | | | CFG\_MSA21\_1 : 0 (rd ok) | [switch13] |
| S2.2 = OFF | | | | CFG\_MSA22\_0 : 1 (rd ok) | [switch14] |
| S2.2 = ON | | | | CFG\_MSA22\_0 : 0 (rd ok) | [switch14] |
| S2.1 = OFF | | | | CFG\_MSA22\_1 : 1 (rd ok) | [switch15] |
| S2.1 = ON | | | | CFG\_MSA22\_1 : 0 (rd ok) | [switch15] |
| S8.1 = OFF | | | | PRESENCE\_CPU\_E : 1 (rd ok) | [switch16] |
| S8.1 = ON | | | | PRESENCE\_CPU\_E : 0 (rd ok) | [switch16] |
| S8.2 = OFF | | | | PRESENCE\_CPU\_AUX : 1 (rd ok) | [switch17] |
| S8.2 = ON | | | | PRESENCE\_CPU\_AUX : 0 (rd ok) | [switch17] |
| S6.1 = OFF | | | | SLOT\_M\_OU\_E : 1 (rd ok) | [switch18] |
| S6.1 = ON | | | | SLOT\_M\_OU\_E : 0 (rd ok) | [switch18] |
| **Test** **de** **la** **liaison** **RS485** **FDP** | | | | | |
| Choix | 1: RS485 test |  | | Pas d’erreur de transmission | [rs] |
| S4.1 et S4.2 en position ON | | | |
| **Test** **des** **accès** **SATA** | | | | | |
| Choix | 7: SATA Port1 tes | | t | 5 essais de transfert de fichier réussis | [sata1] |
| Choix | 8: SATA Port2 tes | | t | 5 essais de transfert de fichier réussis | [sata2] |
| **Test** **du** **Switch** **et** **des** **accès** **Ethernet** | | | | | |
| Accès CPU-M (RJ45 J20 carte FDP) | | | | Débit > 200 Mbits/s | [debit1] |
| Taux perte datagramme < 1% | [perte1] |
|  | | | | | | |

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| **PV** **de** **TEST** **(page** **3/3)** | | | | | | | | |  |
|  | | | | | | | | |
| **Paramètres** | | | | | | | **Sanctions** | **Mesures** |
|  | | | | | | | | |
| **Test** **de** **la** **DATAFLASH** | | | | | | | | |
| Choix 9 | | : DATAFLASH test | | | |  | Autotest OK | [dataflash] |
| **Test** **du** **RTC** | | | | | | |  |  |
| Choix | r : RTC test | |  | | | | Test de la zone RAM OK | [rtc1] |
|  | | | | | | | Test écriture/lecture heure OK | [rtc2] |
| **Test** **de** **la** **FRAM** | | | | | | | | |
| Choix | f : FRAM test | | |  | | | Autotest OK | [fram] |
| **Test** **du** **capteur** **de** **température** | | | | | | | | |
| Choix | 2 : Temperature | | | |  | | Température DS1621 : OK ? | [ds] |
|  | | | | | | | Température CPU : OK ? | [cpu] |
| **Test** **du** **watchdog** | | | | | | | | |
| Redémarrage du logiciel | | | | | | | OK avec délai < 164 s ? | [watchdog] |
|  | | | | | | | | | |